

Operating Temperature:

-55°C to +125°C

Operating Voltage:

50 VDC, 100 VDC, 200 VDC

Rated Current:

5 Amperes maximum

Dielectric Strength:

Twice DC Operating Voltage @ +25°C,
50 mA maximum charging current.

Insulation Resistance:

Measured with rated DC voltage,
100 megohm-microfarad or 100,000
megohms minimum, whichever is less,
50 mA maximum charging current,
@ +25 after two minutes.

Insertion Loss:

At -55°C to +125°C, the insertion loss
will decrease a maximum of 3 dB from
the +25°C value.

Military Specifications:

Meets or exceeds the applicable
parameters of MIL-PRF-15733 and
MIL-PRF-28861.

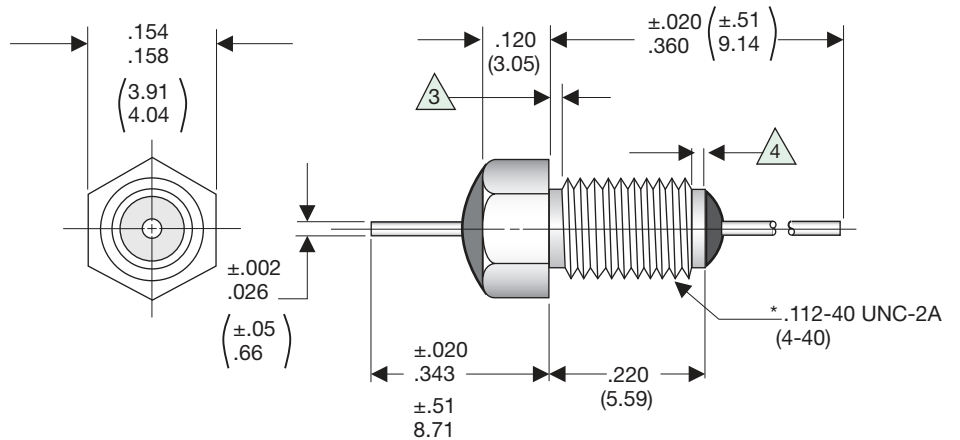
Housing (Non Hermetically Sealed) and Hardware:

Electro-tin-lead plated or hot solder
dipped, and can be supplied with
silver or high purity gold plating.

Marking:

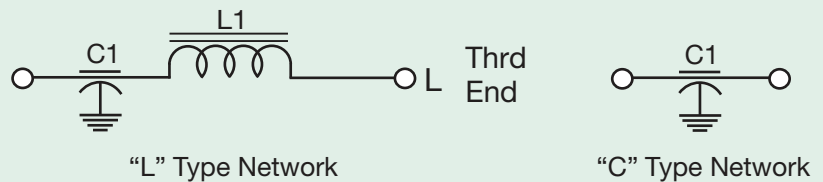
LOGO	PART NUMBER			DATE CODE	
W	CF	XX	XX	XX	XX

4-40 Thread Epoxy Sealed (DC Applications)



Notes:

- Dimensions are inches (mm in parantheses).
- *All units supplied with internal tooth lockwasher and hex nut, dimensions on page 6.
- Imperfect thread or .030 inch maximum undercut optional.
- One imperfect thread allowed, .030 inches maximum.
- Recommended mounting torque 36 oz-in maximum.
- Potting shall not extend beyond .030 inches from body.
- Tolerance: $\pm .010$ ($\pm .254$ mm) unless otherwise specified.
- Alternate capacitance values and various mechanical configurations available upon request.



PART NUMBER	CAPACITANCE μF +100%, - 0%	CIRCUIT	WORKING VOLTAGE DC VOLTS	MINIMUM INSERTION LOSS (Db) At +25°C IN ACCORDANCE WITH MIL-STD-220 ^{1,2}					
				1. MHz	10 MHz	100 MHz	200 MHz	1 GHz	10 GHz
CF 6-001	0.075	L	50	18	37	52	64	70	70
CF 6-003	0.027	L	100	10	30	50	54	70	70
CF 6-004	0.050	L	100	15	38	54	60	70	70
CF 6-008	0.010	L	200	4	21	35	42	64	70
CF 6-002	0.027	C	100	10	30	39	45	65	70
CF 6-005	1,000 pF	C	200	-	4	20	25	50	55
CF 6-006	5,000 pF	C	200	-	15	34	41	50	55
CF 6-007	0.010	C	200	4	21	35	42	65	70

1 Insertion loss measurements shall be made under full load over the frequency range of 1.0 MHz to 10 MHz. Insertion loss measurements above this frequency range under no load.
2 The insertion loss requirements between any two adjacent specified frequencies shall be that of the lower of the two frequencies in order to accommodate resonant dips.