Operating Temperature:
-55ºC to +125ºC

Operating Voltage:
100 VDC, 200 VDC, 125 VAC, 400 Hz

Peak Transient Voltage:
10µsec) @ +25ºC.

Dielectric Strength:
Twice DC Operating Voltage @ +25ºC, 50 mA maximum charging current.

Insulation Resistance:
Measured with 100 VDC or the operating voltage, whichever is less, 50 mA maximum charging current, @ +25 after two minutes.

Insertion Loss:
At -55ºC to +125ºC, the insertion loss will decrease a maximum of -3 dB from the +25ºC value.

Military Specifications:
Meets or exceeds the applicable parameters of MIL-PRF-15733 and MIL-PRF-28861.

Housing (Hermetically Sealed):
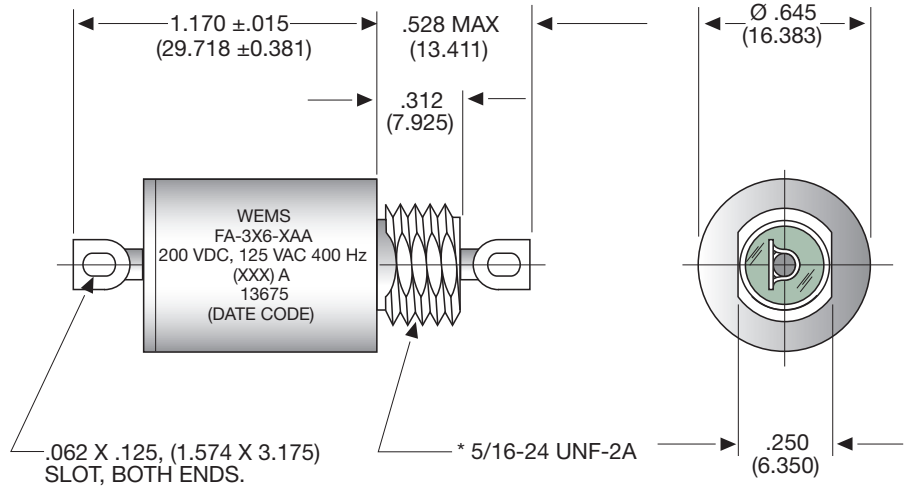Electro-tin-lead plated, hot solder dipped, or as specified.

Recommended Torque:
64 inch/oz. maximum.

Marking:
WEMS, part number, Federal Code identification, voltage, current and date code.
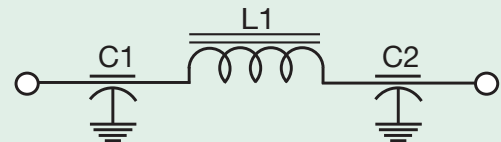
High Performance (AC/DC Applications)

Tolerance: ± .010 (±.254 mm) unless otherwise specified.

*All units supplied with internal tooth lockwasher and hex nut.

For further details see page 6.

Dimensions are inches (mm in parantheses).

π Type Network

PART NUMBER	CURRENT DC MAX. (AMPERES)	RESISTANCE DC MAX. (Ω)	WORKING VOLTAGE		INSULATION RESISTANCE (MΩ MIN)	PEAK TRANSIENT VOLTAGE (10µSEC)	MINIMUM INSERTION LOSS (Db) At +25ºC IN ACCORDANCE WITH MIL-STD-220 [1][2]						
			DC	AC 400Hz			30 kHz	150 kHz	300 kHz	1.0 MHz	10 MHz	100 MHz	1.0 GHz
FA-316-AAA	0.5	0.30	100		100	300	37	78	80	80	80	80	80
FA-316-GAA	1.0	0.21	100		100	300	27	72	80	80	80	80	80
FA-316-JAA	3.0	0.03	100		100	300	–	51	69	80	80	80	80
FA-316-KAA	5.0	0.02	100		100	300	–	41	61	80	80	80	80
FA-326-FAA	0.5	0.30	200	125	300	500	19	64	80	80	80	80	80
FA-326-GAA	1.0	0.21	200	125	300	500	8	57	73	80	80	80	80
FA-326-JAA	3.0	0.03	200	125	300	500	–	34	54	80	80	80	80
FA-326-KAA	5.0	0.02	200	125	300	500	–	20	43	78	80	80	80

1 Insertion loss measurements shall be made under full load over the frequency range of 1.0 MHz to 10 MHz. Insertion loss measurements above this frequency range under no load.
2 The insertion loss requirements between any two adjacent specified frequencies shall be that of the lower of the two frequencies in order to accommodate resonant dips.