

**Operating Temperature:**

-55°C to +125°C

**Operating Voltage:**

50 VDC

**Peak Transient Voltage:**

150 VDC (10µsec) @ +25°C.

**Dielectric Strength:**

100 VDC @ +25°C, 50 mA maximum charging current.

**Insulation Resistance:**

170 megohms minimum with 50 VDC, 50 mA maximum charging current, @ +25 after 2 minutes.

**Insertion Loss:**

At -55°C to +125°C, the insertion loss will decrease a maximum of -3 dB from the +25°C value.

**Military Specifications:**

Meets or exceeds the applicable parameters of MIL-PRF-15733 and MIL-PRF-28861.

**Housing (Hermetically Sealed):**

Electro-tin-lead plated, hot solder dipped, or as specified.

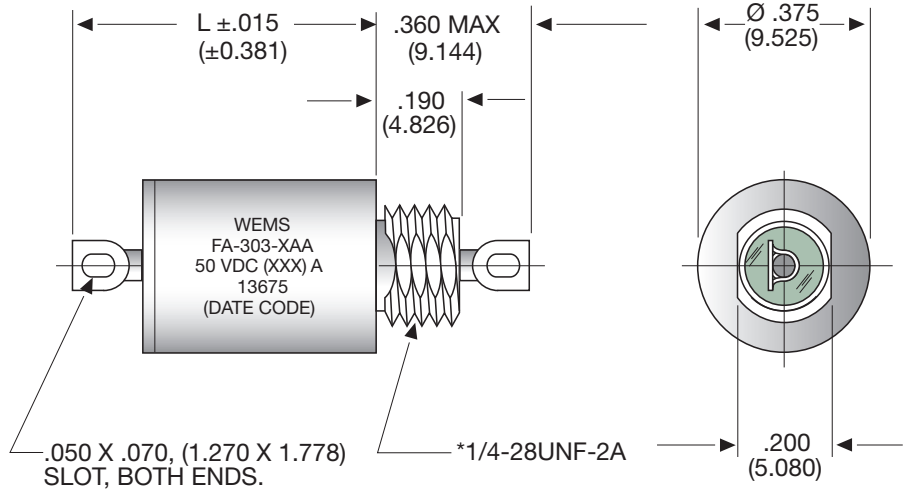
**Recommended Torque:**

48 inch/oz. maximum.

**Marking:**

WEMS, part number, Federal Code identification, voltage, current and date code.

## High Performance (50 VDC Applications)

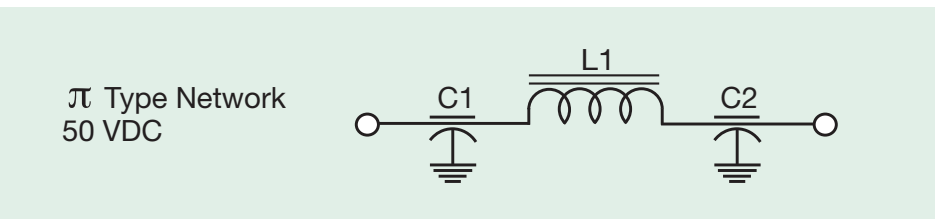


Tolerance: ± .010 (±.254 mm) unless otherwise specified.

\*All units supplied with internal tooth lockwasher and hex nut.

For further details see page 6.

Dimensions are inches (mm in parantheses).



PART NUMBER	CURRENT DC MAX. (AMPERES)	RESISTANCE DC MAX. (Ω)	DIMENSION L INCHES (MM)	MINIMUM INSERTION LOSS (Db) At +25°C IN ACCORDANCE WITH MIL-STD-220 <sup>1,2</sup>						
				30 kHz	150 kHz	300 kHz	1.0 MHz	10 MHz	100 MHz	1.0 GHz
FA-303-AAA	0.06	7.00	.721 (18.313)	57	80	80	80	80	80	80
FA-303-BAA	0.15	3.00	.721 (18.313)	47	80	80	80	80	80	80
FA-303-CAA	0.25	2.90	.721 (18.313)	42	80	80	80	80	80	80
FA-303-FAA	0.50	0.65	.721 (18.313)	27	69	80	80	80	80	80
FA-303-GAA	1.00	0.36	.721 (18.313)	-	57	75	80	80	80	80
FA-303-HAA	2.00	0.12	.721 (18.313)	-	47	65	80	80	80	80
FA-303-JAA	3.00	0.04	.721 (18.313)	-	35	56	80	80	80	80
FA-303-KAA	5.00	0.007	.721 (18.313)	-	-	45	75	80	80	80
FA-303-LAA	10.00	0.005	.721 (18.313)	-	-	29	53	80	80	80
FA-303-MAA	15.00	0.0025	.721 (18.313)	-	-	29	53	80	80	80

1 Insertion loss measurements shall be made under full load over the frequency range of 1.0 MHz to 10 MHz. Insertion loss measurements above this frequency range under no load.  
 2 The insertion loss requirements between any two adjacent specified frequencies shall be that of the lower of the two frequencies in order to accommodate resonant dips.